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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Serial No.: 10/602,369 Conf. No.: 1172
Examiner: N/A Art Unit: 2121
Filing Date: 6/24/2003 Docket No.: EEND920030008US1
(IBME-0077)
Applicant: Dotson et al.
Title: SYSTEM, METHOD AND PROGRAM PRODUCT FOR POSITIONING I/O
PADS ON A CHIP

COMMISSIONER FOR PATENTS

Commissioner for Patents
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STATUS INQUIRY

Sir:

Applicant's representative respectfully requests the status of the above-referenced patent application.

Respectfully submitted,

Date: June 27, 2005

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CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office on the date shown below.

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June 27, 2005

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